

Abstract of the Disclosure

A current reference with reduced sensitivity to process variations includes a variable resistor and a control transistor. The control transistor has a current from source-to-drain that is provided by a current mirror. The current mirror also provides
5 a current to a variable resistor that is coupled gate-to-source to the control transistor. A control loop circuit measures the reference current provided by the current mirror and modifies the resistance value of the variable resistor in response. An external precision resistor is used to measure the reference current, and current variations as a result of process variations are reduced.

10

"Express Mail" mailing label number: EV041119755US
Date of Deposit: November 26, 2001

This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to U.S. Patent and Trademark Office, Box Patent Application, P.O. Box 2327, Arlington, VA 22202.